

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: 03-1202/LSI1P233

First Named Inventor: KAMATH



Addressee" service under 37 CFR §1.10, Mailing Label Number EV332826317US, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service

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UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors: Sir:

Arvind Kamath, Venkatesh P. Gopinath, Wen-Chin Yeh and David Pachura

LOW VOLTAGE ANTIFUSE DEVICE AND PROCESS For: Assigned to: LSI Logic Corporation Application Elements: 24 Pages of Specification, Claims and Abstract 10 Sheets of formal Drawings 03 Pages Combined Declaration and Power of Attorney Accompanying Application Parts: Do not publish this application. Nonpublication Request is attached. Assignment and Assignment Recordation Cover Sheet 37 CFR 3.73(b) Statement by Assignee Information Disclosure Statement with Form PTO-1449 Copies of IDS Citations Preliminary Amendment Return Receipt Postcard Other:

Claim For	Foreign Priority					
Pri	is claimed unde The certifi The certifi	ed copy is end	closed. een filed in prior ap	plication U.S. Ap	oplication	No.
Fee Calcu	lation (37 CFR § 1.	<u>16)</u>				
Ap	oplicant is entitled to	Small Entity	Status under 37 C.l	F.R. §1.27.		
	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee
TOTAL	18	MINUS	20	= 0	x 18 =	\$-0-
INDEP.	3	MINUS	3	= 0	x 86 =	\$-0-
		<u> </u>	ation of multiple de	pendent claim	\$280 =	100,04(0).
Basic Filing Fee under 37 C.F.R. §1.16(a) \$770 =						\$770.00
TOTAL						\$770.00
SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)						
No. 12-22 General A Applifor any su	Commissioner is aut 52 (Order No. 03-12 authorization for Pet icants hereby make bsequent filings. The nay be needed to De	202/LSI1P233 ition for Exter and generally ne Commissio). nsion of Time (37 C authorize any Petiti ner is also authorize	CFR §1.136) ons for Extension ed to charge any	ns of Time	e as may be needed fees under 37 CFR
Nea Plea	se send corresponde	ence to the foll	owing address:			
Date: Oc		1 15 M	LSI Logic Corporate 51 McCarthy Bould ilpitas, California 9 lephone (408) 433-	ion evard 5035		

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: KAMATH et al.

Attorney Docket No.: 03-1202/LSI1P233

Application No.: Not yet assigned

Examiner: Not yet assigned

Filed: Herewith

Group: Not yet assigned

Title: LOW VOLTAGE ANTIFUSE DEVICE

AND PROCESS

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Signed:

Ryan Eachus

NONPUBLICATION REQUEST AND CERTIFICATION (35 U.S.C. 122(b)(2)(B)(i))

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen (18) months after filing. I hereby request that the attached application **NOT** be published under 35 U.S.C. 122(b).

Date: 10.29.0

Russell N. Swerdon Registration No. 36,943

NOTE:

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications, eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign of international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).